BED OF THE PERSON OF THE PERSO

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)

ATTY. DOCKET NO. 004066 USA D01/Consilium/Consilium SERIAL NO. 10/084,092

RECEIVED

OCT 1 0 2003

APPLICANT Technology Center 2100
John F. ARACKAPARAMBIL et al.

FILING DATE G February 28, 2002 2

GROUP 2121

	:		C DAMENIO	DOCUMENTO :		1 1 H W 1 1		
		U	.S. PATENT	DOCUMENTS			1.15	
EXAMINER'S INITIALS	PÄTENT NO.	DATE		NAME	CLASS	SUBCLASS	FILING	DATE
SRG	5,901,313	05/04/99	Wolf et al.				09/02/	97
SRG	6,002,989	12/14/99	Shiba et al.				04/01/	97
SRG	6,094,688	07/25/00	Mellen-Garr	nett et al.			03/12/	98
SRG	6,340,602	01/22/02	Johnson et a	1.			02/12/	01
SRG	6,345,288	02/05/02	Reed et al.				05/15/	00
SRG	6,368,879	04/09/02	Toprac				09/22/	99
SRG	US-2002/0107604	08/08/02	Riley et al.				12/06/	00
SRG	6,470,230	10/22/02	Toprac et al.				01/04/	00
SRG	6,482,660	11/19/02	Conchieri et	al.			03/19/	01
SRG	6,567,717	05/20/03	Krivokapic (et al.			01/19/	00
		 FOR	 EIGN PATE	NT DOCUMENTS				
EXAMINER'S INITIALS	PATENT NO.	DATE		COUNTRY	CLASS	SUBCLASS		lation
SRG	WO 99/59200	·11/18/99	WIPO				x	
SRG	WO 01/52319	07/19/01	WIPO				Х	
	OTHER	ART (Inclu	 ding Author,	Title, Date, Pertiner	l it Pages, Etc) .)		
SRG	Shanthikumar. Octo	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Williams, Randy, Dadi Gudmundsson, Kevin Monahan, Raman Nurani, Meryl Stoller and J. George Shanthikumar. October 1999. "Optimized Sample Planning for Wafer Defect Inspection," Semiconductor Manufacturing Conference Proceedings, 1999 IEEE International Symposium on Santa Clara, CA. Piccataguay NJ, pp. 43, 46						
SRG	23 July 2003. Invitation to Pay Additional Fees and Communication Relating to the Results of the Partial International Search for PCT/US02/19116.							
SRG	01 August 2003. V	Vritten Opinio	on for PCT/US	S01/27406.				
SRG	20 August 2003. V	Vritten Opinio	on for PCT/U	501/22833.				
EXAMINER	/Steve	n Garlan	d/	DATE CONSIDER	ED 06	/22/2006		



PTC/SB/08a/b (07-05)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of Information unless it contains a valid OMB control number. Complete If Known Substitute for form 1449A/B/PTO Application Number 10/084,092 INFORMATION DISCLOSURE February 28, 2002 Filing Date STATEMENT BY APPLICANT First Named Inventor Arackaparambil et al. Art Unit 2125 (Use as many sheets as necessary) Steven Garland Examiner Name 4066 USA D1/Consilium/Consilium Sheet 1 of 1 Attorney Docket Number

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ³ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	۳	

"EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 'Applicant's unique citation designation number (optional). *See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. *Senter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. *Skind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

	NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
SRG		"NanoMapper wafer nanotopography measurement by ADE Phase Shift." http://www.phase-shift.com/nanomap.shtml. December 9, 2003.					
SRG		"Wafer flatness measurement of advanced wafers." http://www.phase-shift.com/wafer-flatness.shtml." December 9, 2003.					
SRG		"ADE Technologies, Inc 6360." http://www.adetech.com/6360.shtml." December 9, 2003.					
SRG		"3D optical profilometer MicroXAM by ADE Phase Shift." http://www.phase-shift.com/microxam.shtml. December 9, 2003.					
SRG		"NanoMapper FA factory automation wafer nanotopography measurement." http://www.phase-shift.com/nanomapperfa.shtml." December 9, 2003.					

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Examiner Signature	/Steven Garland/	Date Considered	06/22/2006
5660017			

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

SHEET 1 OF 11

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)

ATTY. DOCKET NO. 004066 USA D01/Consilium/Consilium

SERIAL NO. 10/084,092

APPLICANT

John F. ARACKAPARAMBIL et al.

FILING DATE February 28, 2002

GROUP 2121

A STATE OF THE STA			U.S. PATEN	DOCUMENTS	: 4-709		****
EXAMINER'S INITIALS	PATENT NO.	DATE		NAME	CLASS	SUBCLASS	FILING DATE
SRG	3,205,485	09/07/65	Noltingk				10/21/60
	3,229,198	01/11/66	Libby				09/28/62
	4,000,458	12/28/76	Miller et al	•			08/21/75
	4,302,721	11/24/81	Urbanek et	al.			05/15/79
	4,750,141	06/07/88	Judell et al.	•			11/26/85
	4,757,259	07/12/88	Charpentie				11/05/86
	4,938,600	07/03/90	Into		1		02/09/89
	5,283,141	02/01/94	Yoon et al.	·			03/05/92
	5,338,630	08/16/94	Yoon et al.				11/18/93
	5,485,082	01/16/96	Wisspeintn	er et al.			04/05/90
	5,497,381	03/05/96	O'Donoghi	ie et al.			06/01/95
	5,511,005	04/23/96	Abbe et al.				02/16/94
	5,519,605	05/21/96	Cawlfield			·	10/24/94
	5,526,293	06/11/96	Mozumder	et al.		·	12/17/93
	5,541,510	06/30/96	Danielson	-			04/06/95
	5,546,312	08/13/96	Mozumder	et al.			02/24/94
	5,553,195	09/03/96	Meijer	· · · · · · · · · · · · · · · · · · ·		•	09/29/94
	5,602,492	02/11/97	Cresswell e	t al.			04/28/94
	5,617,023	04/01/97	Skalski				02/02/95
	5,627,083	05/06/97	Tounai				05/12/95
	5,642,296	06/24/97	Saxena				07/29/93
	5,646,870	07/08/97	Krivokapic	et al.			02/13/95
	5,649,169	07/15/97	Berezin et a	1.			06/20/95
	5,654,903	08/05/97	Reitman et	al.			11/07/95
1	5,663,797	09/02/97	Sandhu				05/16/96
V	5,665,199	09/09/97	Sahota et al	•			06/23/95
SRG	5,666,297	09/09/97	Britt et al.				05/13/94
EXAMINER	KAMINER /Steven Garland/ DATE CONSIDERED 06/22/20				/2006		

SHEET 2 OF 11

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)

ATTY. DOCKET NO. 004066 USA D01/Consilium/Consilium SERIAL NO. 10/084,092

APPLICANT

John F. ARACKAPARAMBIL et al.

FILING DATE
February 28, 2002

GROUP 2121

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILIN
SRG	5,667,424	09/16/97	Pan			09/25/9
1	5,674,787	10/07/97	Zhao et al.			01/16/9
	5,719,796	02/17/98	Chen			12/04/9
	5,735,055	04/07/98	Hochbein et al.			04/23/9
	5,761,064	06/02/98	La et al.		<u> </u>	10/06/9
	5,777,901	07/07/98	Berezin et al.			09/29/9
	5,787,021	07/28/98	Samaha			12/18/9
	5,787,269	07/28/98	Hyodo		 -	09/19/9
	5,825,913	10/20/98	Rostami et al.			07/18/9
	5,857,258	01/12/99	Penzes et al.			05/12/9
	5,910,846	06/08/99	Sandhu			08/19/9
	5,943,237	08/24/99	Van Boxem			10/17/9
	5,960,185	09/28/99	Nguyen		·	06/24/9
	5,961,369	10/05/99	Bartels et al.			06/04/9
	5,978,751	11/02/99	Pence et al.		· · · · · · · · · · · · · · · · · · ·	02/25/9
	6,017,771	01/25/00	Yang et al.			04/27/9
	6,036,349	03/14/00	Gombar			07/26/9
	6,064,759	05/16/00	Buckley et al.			11/06/9
	6,072,313	06/06/00	Li et al.			06/17/9
	6,097,887	08/01/00	Hardikar et al.			10/27/97
	6,108,092	08/22/00	Sandhu ·			06/08/99
	6,127,263	10/03/00	Parikh	- 		07/10/98
	6,136,163	10/24/00	Cheung et al.			03/05/99
	6,141,660	10/31/00	Bach et al.			07/16/98
	6,143,646	11/07/00	Wetzel			06/03/97
V	6,148,099	11/14/00	Lee et al.			07/03/97
SRG	6,148,239	11/14/00	Funk et al.			12/12/97

SHEET 3 OF 11

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)

ATTY. DOCKET NO. 004066 USA D01/Consilium/Consilium SERIAL NO. 10/084,092

APPLICANT

John F. ARACKAPARAMBIL et al.

FILING DATE February 28, 2002

GROUP 2121

NAME	CLASS	SUBCLASS	FILIN DATI
Zhang			10/13/9
Satoh et al.			03/06/9
Rosenthal et al.			09/17/9
Runnels			06/29/9
Chalmers et al.			12/11/9
Sepulveda et al.			11/02/9
Sandhu			02/29/0
Ghoshal			06/24/9
Atchison et al.			06/15/9
Bothra et al.			11/20/9
Campbell et al.			08/11/99
Phan et al.			09/13/99
Sandhu et al.			12/29/0
Finarov			12/04/00
Klimasauskas			05/27/99
Campbell et al.		·	08/11/99
Wiswesser et al.			11/02/98
Campbell et al.			10/25/99
Gonzales et al.		 	08/11/99
Hofmann			03/23/00
Toprac et al.			10/23/00
Nakano et al.			03/07/01
Lu et al.			08/18/00
Hu et al.			12/04/97
Mendez et al.			04/02/01
Matsushita et al.			03/15/00
	Matsushita et al.	Matsushita et al.	

SHEET 4 OF 11

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)

ATTY. DOCKET NO. 004066 USA D01/Consilium/Consilium

SERIAL NO. 10/084,092

APPLICANT

John F. ARACKAPARAMBIL et al.

FILING DATE February 28, 2002 GROUP 2121

XAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILIN
SRG	2001/0042690	11/22/01	Talieh	-		12/14/0
I	6,324,481	11/27/01	Atchison et al.			06/15/9
	6,334,807	01/01/02	Lebel et al.	 		04/30/9
	6,336,841	01/08/02	Chang			03/29/0
7	2002/0032499	03/14/02	Wilson et al.			05/04/0
	6,360,133	03/19/02	Campbell et al.			06/17/9
	6,360,184	03/19/02	Jacquez			03/26/9
	6,368,883	04/09/02	Bode et al.			08/10/9
	6,368,884	04/09/02	Goodwin et al.	 		04/13/0
	6,379,980	04/30/02	Toprac	- 		07/26/0
	6,388,253	05/14/02	Su			11/02/0
	2002/0058460	05/16/02	Lee et al.	1		09/14/0
	6,395,152	05/28/02	Wang	 		07/02/9
	6,397,114	05/28/02	Eryurek et al.	1		05/03/9
	6,405,096	06/11/02	Toprac et al.			08/10/9
	6,405,144	06/11/02	Toprac et al.			01/18/0
	2002/0070126	06/13/02	Sato et al.			09/19/0
	2002/0081951	06/27/02	Boyd et al.			02/20/0
	2002/0089676	07/11/02	Pecen et al.			04/26/0
	2002/0102853	08/01/02	Li et al.			12/20/0
	2002/0107599	08/08/02	Patel et al.			01/25/0
	6,435,952	08/20/02	Boyd et al.			06/30/00
	6,438,438	08/20/02	Takagi et al.			01/02/98
	2002/0113039	08/22/02	Mok et al.			02/16/01
	6,440,295	08/27/02	Wang	† †		02/04/00
V	2002/0127950	09/12/02	Hirose et al.			03/08/01
SRG	6,455,937	09/24/02	Cunningham			03/17/99
AMINER	/5	teven Gar	land/ DATE CONSIDERE	'D	06/22/200	6

SHEET 5 OF 11 ATTY. DOCKET NO. SERIAL NO. 004066 USA 10/084,092 INFORMATION DISCLOSURE D01/Consilium/Consilium CITATION IN AN APPLICATION (PTO-1449) APPLICANT John F. ARACKAPARAMBIL et al. FILING DATE GROUP February 28, 2002 2121 U.S. PATENT DOCUMENTS **EXAMINER'S** FILING INITIALS PATENT NO. DATE CLASS NAME SUBCLASS DATE 2002/0149359 10/17/02 Crouzen et al. SRG 08/18/01 6,479,902 11/12/02 Lopatin et al. 06/29/00 6,479,990 11/12/02 Mednikov et al. 06/18/01 2002/0185658 12/12/02 Inoue et al. 06/14/01 2002/0193902 12/19/02 Shanmugasundram et al. 06/18/02 2002/0197745 12/26/02 Shanmugasundram et al. 08/31/01 2002/0197934 12/26/02 Paik 11/30/01 2002/0199082 12/26/02 Shanmugasundram et al. 06/18/02 6,503,839 01/07/03 Gonzales et al. 07/03/01 2003/0020909 01/30/03 Adams et al. 04/09/01 2003/0020928 01/30/03 Ritzdorf et al. 07/09/01 6,517,413 02/11/03 Hu et al. SRG 10/25/00 FOREIGN PATENT DOCUMENTS **EXAMINER'S** INITIALS PATENT NO. DATE COUNTRY CLASS SUBCLASS Yes No 61-66104 04/04/86 Japan X SRG 3-202710 09/04/91 Japan X 8-23166 01/23/96 Japan X 9-246547 09/19/97 Japan X WO 98/05066 02/05/98 WIPO X 10-34522 02/10/98 Japan X 0 869 652 10/07/98 Europe $\overline{\mathbf{x}}$ WO 99/09371 02/25/99 WIPO X 0 910 123 04/21/99 Europe X 0 932 194 07/28/99 Europe X WO 00/00874 01/06/00 WIPO X 2000-183001 06/30/00 SRG Japan

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

/Steven Garland/

EXAMINER

DATE CONSIDERED

06/22/2006

SHEET <u>6</u> OF <u>11</u> ATTY, DOCKET NO. SERIAL NO. 004066 USA 10/084,092 INFORMATION DISCLOSURE D01/Consilium/Consilium CITATION IN AN APPLICATION (PTO-1449) APPLICANT John F. ARACKAPARAMBIL et al. FILING DATE GROUP February 28, 2002 2121 FOREIGN PATENT DOCUMENTS* **EXAMINER'S** INITIALS PATENT NO. DATE COUNTRY **CLASS** SUBCLASS No 1 071 128 01/24/01 Europe SRG X WO 01/18623 03/15/01 WIPO X WO 01/25865 04/12/01 WIPO X 434103 05/16/01 Taiwan X 436383 05/28/01 Taiwan X 455938 09/21/01 Taiwan X 455976 09/21/01 Taiwan $\overline{\mathbf{X}}$ 2001-284299 10/12/01 Japan $\overline{\mathbf{x}}$ 2001-305108 10/31/01 Japan X 2002-9030 01/11/02 Japan X WO 02/074491 09/26/02 WIPO $\overline{\mathbf{x}}$ 2002-343754 11/29/02 Japan SRG OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Ostanin, Yu.Ya. October 1981. "Optimization of Thickness Inspection of Electrically Conductive Single-Layer Coatings with Laid-on Eddy-Current Transducers (Abstract)." Defektoskopiya, vol. 17, no. 10, pp. SRG 45-52. Moscow, USSR. February 1984. "Substrate Screening Process." IBM Technical Disclosure Bulletin, pp. 4824-4825. Herrmann, D. 1988. "Temperature Errors and Ways of Elimination for Contactless Measurement of Shaft Vibrations (Abstract)." Technisches MessenTM, vol. 55, no. 1, pp. 27-30. West Germany. Lin, Kuang-Kuo and Costas J. Spanos. November 1990. "Statistical Equipment Modeling for VLSI Manufacturing: An Application for LPCVD." IEEE Transactions on Semiconductor Manufacturing, v. 3, n. Chang, Norman H. and Costas J. Spanos. February 1991. "Continuous Equipment Diagnosis Using Evidence Integration: An LPCVD Application." IEEE Transactions on Semiconductor Manufacturing, v. 4, n. 1, pp. 43-51. Larrabee, G. B. May 1991. "The Intelligent Microelectronics Factory of the Future (Abstract)." IEEE/SEMI International Semiconductor Manufacturing Science Symposium, pp. 30-34. Burlingame, CA. Burke, Peter A. June 1991. "Semi-Empirical Modelling of SiO2 Chemical-Mechanical Polishing SRG Planarization." VMIC Conference, 1991 IEEE, pp. 379-384. IEEE.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

/Steven Garland/

EXAMINER

06/22/2006

DATE CONSIDERED

SHEET 7 OF 1 ATTY, DOCKET NO. SERIAL NO. 004066 USA 10/084,092 INFORMATION DISCLOSURE D01/Consilium/Consilium CITATION IN AN APPLICATION (PTO-1449) APPLICANT John F. ARACKAPARAMBIL et al. FILING DATE GROUP February 28, 2002 2121 OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) May 1992. "Laser Ablation Endpoint Detector." IBM Technical Disclosure Bulletin, pp. 333-334. SRG Spanos, Costas J., Hai-Fang Guo, Alan Miller, and Joanne Levine-Parrill. November 1992. "Real-Time Statistical Process Control Using Tool Data." IEEE Transactions on Semiconductor Manufacturing, v. 5, n. 4, pp. 308-318. February 1993. "Electroless Plating Scheme to Hermetically Seal Copper Features." IBM Technical Disclosure Bulletin, pp. 405-406. Scarr, J. M. and J. K. Zelisse. April 1993. "New Topology for Thickness Monitoring Eddy Current Sensors (Abstract)." Proceedings of the 36th Annual Technical Conference, Dallas, Texas. Matsuyama, Akira and Jessi Niou. 1993. "A State-of-the-Art Automation System of an ASIC Wafer Fab in Japan." IEEE/SEMI International Semiconductor Manufacturing Science Syposium, pp. 42-47. Yeh, C. Eugene, John C. Cheng, and Kwan Wong. 1993. "Implementation Challenges of a Feedback Control System for Wafer Fabrication." IEEE/CHMT International Electronics Manufacturing Technology Symposium, pp. 438-442. Kurtzberg, Jerome M. and Menachem Levanoni. January 1994. "ABC: A Better Control for Manufacturing." IBM Journal of Research and Development, v. 38, n. 1, pp. 11-30. Mozumder, Purnendu K. and Gabriel G. Barna. February 1994. "Statistical Feedback Control of a Plasma Etch Process." IEEE Transactions on Semiconductor Manufacturing, v. 7, n. 1, pp. 1-11. Muller-Heinzerling, Thomas, Ulrich Neu, Hans Georg Nurnberg, and Wolfgang May. March 1994. "Recipe-Controlled Operation of Batch Processes with Batch X." ATP Automatisierungstechnische Praxis, vol. 36, no. 3, pp. 43-51. Stoddard, K., P. Crouch, M. Kozicki, and K. Tsakalis. June-July 1994. "Application of Feedforward and Adaptive Feedback Control to Semiconductor Device Manufacturing (Abstract)." Proceedings of 1994 American Control Conference - ACC '94, vol. 1, pp. 892-896. Baltimore, Maryland. Schaper, C. D., M. M. Moslehi, K. C. Saraswat, and T. Kailath. November 1994. "Modeling, Identification, and Control of Rapid Thermal Processing Systems (Abstract)." Journal of the Electrochemical Society, vol. 141, no. 11, pp. 3200-3209. Tao, K. M., R. L. Kosut, M. Ekblad, and G. Aral. December 1994. "Feedforward Learning Applied to RTP of Semiconductor Wafers (Abstract)." Proceedings of the 33rd IEEE Conference on Decision and Control, vol. 1, pp. 67-72. Lake Buena Vista, Florida. Hu, Albert, He Du, Steve Wong, Peter Renteln, and Emmanuel Sachs. 1994. "Application of Run by Run Controller to the Chemical-Mechanical Planarization Process." IEEE/CPMT International Electronics Manufacturing Technology Symposium, pp. 371-378. Spanos, C. J., S. Leang, S.-Y. Ma, J. Thomson, B. Bombay, and X. Niu. May 1995. "A Multistep Supervisory Controller for Photolithographic Operations (Abstract)." Proceedings of the Symposium on Process Control, Diagnostics, and Modeling in Semiconductor Manufacturing, pp. 3-17. Leang, Sovarong, Shang-Yi Ma, John Thomson, Bart John Bombay, and Costas J. Spanos. May 1996. "A Control System for Photolithographic Sequences." IEEE Transactions on Semiconductor Manufacturing, SRG vol. 9, no. 2.

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

/Steven Garland/

EXAMINER

DATE CONSIDERED

06/22/2006

SHEET 8 OF 11

				SHEET & OF II
			ATTY. DOCKET NO.	SERIAL NO.
ļ .	TNIE	ORMATION DISCLOSURE	004066 USA	10/084,092
	ПЛГ	_	D01/Consilium/Consilium	1
1		CITATION IN AN		
H		APPLICATION		
				1
1		(PTO-1449)		
	•	•	APPLICANT	
1			John F. ARACKAPARAMBI	L et al.
		•		
1			FILING DATE	GROUP
			February 28, 2002	2121
288823	W//W	OTHER ART (Including Author, Ti	tle Date Pertinent Pages Fte	5-4-76-30-0-30-0-30-0-30-0-30-0-30-0-30-0-3
2000	388 88.00	s of activities with the contract of the contr	egger de la company de la comp	ST - No. 17 (2017) (2017) (2017) (2018) (2018) (2018) (2018)
		Boning, Duane S., William P. Moyne, Taber H. S		
5	SRG	Scott Shellman, and John Taylor. October 1996.		
	_	IEEE Transactions on Components, Packaging, a	and Manufacturing Technology-	<i>—Part C</i> , vol. 19, no. 4, pp.
	↓	307-314.		
		Zhe, Ning, J. R. Moyne, T. Smith, D. Boning, E.		
	Ė	"A Comparative Analysis of Run-to-Run Control		
	1	(Abstract)." IEEE/SEMI 1996 Advanced Semicon	nductor Manufacturing Confere	nce Workshop, pp. 375-
	<u> </u>	381.	•	
		Yasuda, M., T. Osaka, and M. Ikeda. December	1996. "Feedforward Control of	a Vibration Isolation
		System for Disturbance Suppression (Abstract)."	Proceeding of the 35th IEEE Co	onference on Decision and
l l		Control, vol. 2, pp. 1229-1233. Kobe, Japan.		
		Fan, Jr-Min, Ruey-Shan Guo, Shi-Chung Chang,	and Kian-Huei Lee, 1996, "Ab	normal Tred Detection of
		Sequence-Disordered Data Using EWMA Method		
		Conference, pp. 169-174.		oo.aa.o.o.
ļ —	1	Smith, Taber and Duane Boning. 1996. "A Self-	Tuning FWMA Controller Utili	zing Artificial Neural
		Network Function Approximation Techniques."		
		Technology Symposium, pp. 355-363.	ELLE CI MI Imeriano an Licci	Tomes Managacianing
1	 	Guo, Ruey-Shan, Li-Shia Huang, Argon Chen, an	d Jin-Jung Chen, October 1907	"A Cost Effective
		Methodology for a Run-by-Run EWMA Controlle		
ļ	1	Manufacturing, pp. 61-64.	ci. O international symposiur	n on semiconaucioi
 	 		October 1007 #A - Freduction	of Madal Davidation
Ì	ı	Mullins, J. A., W. J. Campbell, and A. D. Stock.		
	l	Control in Run-to-Run Processing in Semiconduc		
 	-	The International Society for Optical Engineering		
Į.		Reitman, E. A., D. J. Friedman, and E. R. Lory. 1		U 1
	1	Multiple-System Models for Yield Analysis (Abs	tract)." IEEE Transactions on S	emiconductor
	 	Manufacturing, vol. 10, no. 4, pp. 469-481.		
1		Durham, Jim and Myriam Roussel. 1997. "A Sta		
]	┡	Probe Yield." IEEE/SEMI Advanced Semiconduc		
	1	Shindo, Wataru, Eric H. Wang, Ram Akella, and		
ł		Source Isolation in Defect Inspection and Classifi	cation." 2nd International Work	shop on Statistical
		Metrology, pp. 90-93.		
		July 1998. "Active Controller: Utilizing Active D	Patabases for Implementing Mult	tistep Control of
		Semiconductor Manufacturing (Abstract)." IEEE		
L \	1	Manufacturing Technology-Part C, vol. 21, no.		
	V	Fang, S. J., A. Barda, T. Janecko, W. Little, D. O.		orrison, G. B. Shinn, and
		M. Birang. 1998. "Control of Dielectric Chemica		
l S	RG	Based Endpoint Sensor." International Proceeding	nes of the IEEE Interconnect Te	chnology Conference. pp.
		76-78.		
				
		/05 03/		06/22/2006
EXAM	UNER	/Steven Garland/ DA	ATE CONSIDERED	00/22/2000
		1		<u> </u>
<u> </u>				i

	\ . ·		SHEET 9 OF 11				
		ATTY. DOCKET NO.	SERIAL NO.				
INT	FORMATION DISCLOSURE	004066 USA	10/084,092				
11.11	CITATION IN AN	D01/Consilium/Consilium					
}	APPLICATION						
V	(PTO-1449)						
	APPLICANT						
1	·	John F. ARACKAPARAMBII	∠ et al.				
		FILING DATE	GROUP				
		February 28, 2002	2121				
	OTHER ART (Including Author,	Title Date Pertinent Pages Rtc					
3505 2303 A469 JJ	Ouma, Dennis, Duane Boning, James Chung,	The state of the s	<u> </u>				
SRG	Integrated Characterization and Modeling Met	thodology for CMP Dielectric Plana	ark. 1998. An				
	the IEEE 1998 International Interconnect Tec	hnology Conference, pp. 67-69.	rization. Troccetings by				
	Boning, Duane S., Jerry Stefani, and Stephanie	W. Butler. February 1999. "Statis	stical Methods for				
	Semiconductor Manufacturing." Encyclopedia	a of Electrical Engineering, J. G. W	ebster, Ed.				
	McIntosh, John. March 1999. "Using CD-SE	M Metrology in the Manufacture of	Semiconductors				
 	(Abstract)." JOM, vol. 51, no. 3, pp. 38-39.	Principal Principal Action	"O OO				
	Pan, J. Tony, Ping Li, Kapila Wijekoon, Stan Integration and Time Dependent Pattern Effect	i sai, and Fritz Redeker. May 1999.	"Copper CMP				
	Conference, pp. 164-166.	a. ILLE 1999 International Interco	mneci Technology				
	Meckl, P. H. and K. Umemoto. August 1999.	"Achieving Fast Motions in Semico	onductor Manufacturing				
	Machinery (Abstract)." Proceedings of the 19	99 IEEE International Conference of	on Control Applications,				
ļ	vol. 1, pp. 725-729. Kohala Coast, HI.						
	Khan, K., C. El Chemali, J. Moyne, J. Chapple	-Sokol, R. Nadeau, P. Smith, C., an	d T. Parikh. October				
	1999. "Yield Improvement at the Contact Proc IEEE/CPMT Electronics Manufacturing Techn	cess Through Run-to-Run Control (A	Abstract)." 24"				
	Ruegsegger, Steven, Aaron Wagner, James S.	Freudenberg and Dennis S. Griman	November 1000				
	"Feedforward Control for Reduced Run-to-Run	n Variation in Microelectronics Man	ufacturing." IEEE				
	Transactions on Semiconductor Manufacturing	g, vol. 12, no. 4.	_				
	November 1999. "How to Use EWMA to Ach	ieve SPC and EPC Control." Interr	ational Symposium on				
1	NDT Contribution to the Infrastructure Safety						
	http://www.ndt.net/abstract/ndtiss99/data/35.ledgar , T. F., W. J. Campbell, and C. Bode. Details a control of the con						
	Manufacturing." Proceedings of the 38th IEEE	Conference on Decision and Control	roi in Microelectronics				
	4, pp. 4185-4191.	on Decision and Com	os, i nocias, Anteona, Vol.				
	Meckl, P. H. and K. Umemoto. April 2000. "	Achieving Fast Motions by Using Sl	naped Reference Inputs				
	[Semiconductor Manufacturing Machine] (Abs	tract)." NEC Research and Develop	oment, vol. 41, no. 2, pp.				
	232-237.						
	Oechsner, R., T. Tschaftary, S. Sommer, L. Pfi	tzner, H. Ryssel, H. Gerath, C. Baie	r, and M. Hafner.				
l l	September 2000. "Feed-forward Control for a SPIE – The International Society for Optical E	Liulography/Etch Sequence (Abstra	Ct)." Proceedings of the				
	Cheung, Robin. October 18, 2000. "Copper In	nterconnect Technology." AVS/CMI	P User Group Meeting				
	Santa Clara, CA.						
•	Edgar, Thomas F., Stephanie W. Butler, W. Jar	rett Campbell, Carlos Pfeiffer, Chris	stopher Bode, Sung Bo				
SRG	Hwang, K. S. Balakrishnan, and J. Hahn. Nove	ember 2000. "Automatic Control in	Microelectronics				
	Manufacturing: Practices, Challenges, and Poss	Sibilities (Abstract)." Automatica, v	. 36, n. 11.				
EXAMINER	/Steven Garland/	DATE CONODES S					
EAMMINER	/ Desem Garrand/	DATE CONSIDERED	06/22/2006				
H							

			HEET TO OF IT
		ATTY, DOCKET NO. 004066 USA	SERIAL NO. 10/084,092
INFO	DRMATION DISCLOSURE .	D01/Consilium/Consilium	10/004,972
	CITATION IN AN		1
	APPLICATION	'	
	(PTO-1449)	1	1
•	(F10-1443)	APPLICANT	
		John F. ARACKAPARAMBI	L et al.
		FILING DATE	GROUP
		February 28, 2002	2121
	OTHER ART (Including Author, T		
	Khan, S., M. Musavi, and H. Ressom. November	er 2000. "Critical Dimension Con	ntrol in Semiconductor
SRG	Manufacturing (Abstract)." ANNIE 2000. Smart	t Engineering Systems Design Co	njerence, pp. 993-1000.
	St. Louis, Missouri. ACM Research Inc. 2000. "Advanced Copper"	Metallization for 0.13 to 0.05 μm	& Beyond."
	-http://ecmrc.com/press/ACM-ECP-brochure.pd	if>	
	Ravid, Avi, Avner Sharon, Amit Weingarten, V	ladimir Machavariani, and David	Scheiner. 2000. "Copper
	CMP Planarity Control Using ITM." IEEE/SEN 437-443.	11 Aavancea Semiconductor Man	ujacturing Conjerence, pp.
	Chen, Argon and Ruey-Shan Guo, February 20	01. "Age-Based Double EWMA	Controller and Its
	Application to CMP Processes." IEEE Transac	tions on Semiconductor Manufac	turing, vol. 14, no. 1, pp.
	Tobin, K. W., T. P. Karnowski, L. F. Arrowood	, and F. Lakhani. April 2001. "F	ield Test Results of an
	Automated Image Retrieval System (Abstract)." 2001 IEEE/SEMI, Munich, Germany.		
	Tan, K. K., H. F. Dou, and K. Z. Tang. May-Ju	ne 2001. "Precision Motion Con	trol System for Ultra-
	Precision Semiconductor and Electronic Compo Components and Technology Conference 2001.	Proceedings on 1372-1379. Of	lando. Florida.
	Heuberger, U. September 2001. "Coating Thic Magnetic Inductance Instrument (Abstract)." G	kness Measurement with Dual-Fu	inction Eddy-Current &
	Wang, LiRen and Hefin Rowlands. 2001. "A N International Conference on Emerging Technol	Novel NN-Fuzzy-SPC Feedback (Control System." 8" IEEE
	Moyne, J., V. Solakhian, A. Yershov, M. Ander	rson, and D. Mockler-Hebert. Ap	ril-May 2002.
	"Development and Deployment of a Multi-Com	ponent Advanced Process Contro	ol System for an Epitaxy
	Tool (Abstract)." 2002 IEEE Advanced Semico	onductor Manufacturing Conferen	nce and Workshop, pp. 123-
	130. Sarfaty, M., A. Shanmugasundram, A. Schwarn	J. Paik, Jimin Zhang, Rong Pan	M. J. Seamons, H. Li, R.
	Hung, and S. Parikh. April-May 2002. "Advar	ice Process Control Solutions for	Semiconductor
	Manufacturing (Abstract)." 13th Annual IEEE/	SEMI Advanced Semiconductor N	Sanufacturing Conference.
	Advancing the Science and Technology of Semi	iconductor Manufacturing. ASMC	. 2002, pp. 101-100.
	Boston, MA. Campbell, W. J., S. K. Firth, A. J. Toprac, and	T. F. Edgar, May 2002. "A Com	parison of Run-to-Run
	Control Algorithms (Abstract)." Proceedings of	f 2002 American Control Confer	ence, vol. 3, pp. 2150-2155.
V	Good, Richard and S. Joe Oin, May 2002, "St	ability Analysis of Double EWM.	A Run-to-Run Control with
SRG	Metrology Delay." IEEE/CPMT International 363.	Electronics Manufacturing Tech	www.symposium, pp. 333-
EXAMINER	/Steven Garland/	DATE CONSIDERED	06/22/2006
	/Steven Garland/		

SHEET 11 OF 11 ATTY, DOCKET NO. SERIAL NO. 004066 USA 10/084,092 INFORMATION DISCLOSURE D01/Consilium/Consilium CITATION IN AN APPLICATION (PTO-1449) APPLICANT John F. ARACKAPARAMBIL et al. FILING DATE **GROUP** February 28, 2002 2121 OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Smith, Stewart, Anthony J. Walton, Alan W. S. Ross, Georg K. H. Bodammer, and J. T. M. Stevenson. SRG May 2002. "Evaluation of Sheet Resistance and Electrical Linewidth Measurement Techniques for Copper Damascene Interconnect." IEEE Transactions on Semiconductor Manufacturing, vol. 15, no. 2, pp. 214-222. Itabashi, Takeyuki, Hiroshi Nakano, and Haruo Akahoshi. June 2002. "Electroless Deposited CoWB for Copper Diffusion Barrier Metal." IEEE International Interconnect Technology Conference, pp. 285-287. ACM Research, Inc. 2002. "ACM Ultra ECP® System: Electro-Copper Plating (ECP) Deposition." www.acmrc.com/ecp.html Applied Materials, Inc. 2002. "Applied Materials: Information for Everyone: Copper Electrochemical Plating." www.appliedmaterials.com/products/copper_electrochemical_plating.html. KLA-Tencor Corporation. 2002. "KLA Tencor: Press Release: KLA-Tencor Introduces First Production-Worthy Copper CMP In-Situ Film Thickness and End-point Control System: Multi-Million Dollar Order Shipped to Major CMP Tool Manufacturer." www.kla-tencor.com/news_events/press_releases/press_releases2001/984086002.html. Takahashi, Shingo, Kaori Tai, Hiizu Ohtorii, Naoki Komai, Yuji Segawa, Hiroshi Horikoshi, Zenya Yasuda, Hiroshi Yamada, Masao Ishihara, and Takeshi Nogami. 2002. "Fragile Porous Low-k/Copper Integration by Using Electro-Chemical Polishing." 2002 Symposium on VLSI Technology Digest of Technical Papers, Cunningham, James A. 2003. "Using Electrochemistry to Improve Copper Interconnects." March 25, 2003. International Search Report for PCT/US02/24859 prepared by the European Patent Office. Adams, Bret W., Bogdan Swedek, Rajeev Bajaj, Fritz Redeker, Manush Birang, and Gregory Amico. "Full-Wafer Endpoint Detection Improves Process Control in Copper CMP." Semiconductor Fabtech - 12th Edition. Applied Materials, Inc., Santa Clara, CA. Berman, Mike, Thomas Bibby, and Alan Smith. "Review of In Situ & In-line Detection for CMP Applications." Semiconductor Fabtech, 8th Edition, pp. 267-274. "Semiconductor Manufacturing: An Overview." http://users.ece.gatech.edu/~gmay/overview.html SRG **EXAMINER** /Steven Garland/ DATE CONSIDERED 06/22/2006